

Amendment to the Specification

Please replace paragraph at page 3, line 5, to page 4, line 5 with the following amended paragraph:

As shown in Fig. 2, for the purpose of the display and non-display zone control, a control panel is added to form the inventive structure. The system, for example, a CPU or an OS (not shown), first separates display zones and non-display zones on a display panel. For example, this display in Fig. 2 is divided into three regions; the middle region with digits is a display zone while the other two blank regions are non-display zones. Because the middle display region is controlled by the second lamp LED2 of Fig. 2, the system signals the regulator 212 and the regulator 212 enables the signal R2 to be active so as to light the lamp LED2 only. The signals R1, R3 are inactive. Therefore, power is saved. Additionally, power can be saved through the LCD display matrix circuit. At this point, the gate driver receives a control signal VG generated by the signal controller 210, so as to provide power  $V_{DD}$  to the middle region with digits on the matrix circuit 203 while the remaining blank regions are not supplied power, thus also saving power. Compared

*As carried out*

Comparing [[to]] the efficiency of the two[[ The]]power-saving types, the lamp management type is better. Both are used at the same time to achieve the most[[The]] power-saving efficiency. That is, the lamp 22 on/off and the voltage source supply to the matrix circuit 203 are controlled concurrently. Further, if a plurality of display zones are used, as shown in Fig. 3, the plurality of display zones with respect to the zones 1, 3, n-2 and n-1 can be powered through the corresponding lamps LED1, LED3, LEDn-2, and LEDn-1. Alternately, the plurality of display zones can be powered through the gate driver 301 using the corresponding voltage control signals VGL1 or VGH1, VGL3 or VGH3, VGLn-2 or VGHn-2, VGLn-1 or VGHn-1 generated by the signal controller 210. In this example, the high voltage value VGH can be, for example, +15V and the low voltage value VGL can be, for example, 12V. The high and low voltages can be changed as carried out.